



TEST SPECIFICATION

Complementary Silicon Plastic Power Transistor

TIP41A/TIP41B/TIP41C

Package: TO-220

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Parameter	Test Condition	Test Specification	
		Min	Max
V _{CEO(SUS)}	I _C =30mA _{dc} , I _B =0, TIP41A	60V	
	I _C =30mA _{dc} , I _B =0, TIP41B	80V	
	I _C =30mA _{dc} , I _B =0, TIP41C	100V	
I _{CEO}	V _{CE} =30V _{dc} , I _B =0, TIP41A/TIP41B/TIP41C		0.7mA
I _{CES}	V _{CE} =60V, V _{EB} =0, TIP41A		400μA
	V _{CE} =80V, V _{EB} =0, TIP41B		400μA
	V _{CE} =100V, V _{EB} =0, TIP41C		400μA
I _{EBO}	V _{BE} =5V, I _C =0, TIP41A/TIP41B/TIP41C		1.0mA
h _{FE}	I _C =0.3A, V _{CE} =4V	30	-
	I _C =3A, V _{CE} =4V	15	75
V _{CE(sat)}	I _C =6A, I _B =600mA		1.5V
V _{BE(on)}	I _C =6A, V _{CE} =4V		2V

MARKING:

